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 R Gordon, S McNeary, T Ng, Y Roitblum, M Tate - Custom Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - [ieeexplore.ieee.org](#)

 An N-Bus Datapath Compiler for **IC Design** r ... are run using Lsim to verify the **design's** logic ... For regular structures, the datapath **compil-** er can create layouts ...

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 JR Southard - Computer, 1983 - [ieeexplore.ieee.org](#)

 ... There arc at least twso separable aspects to **IC design**. One is the rigorous specification of the integrated circuit's tinctioti. ...

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 R Turner, QD Syst, CA San Jose - IEEE International Workshop on Rapid System Prototyping, ..., 1999 - [ieeexplore.ieee.org](#)

 ... software into a ROM code file, 2) **Compile** hardware **design** ... doing ad hoc testing of the whole **IC** at once ... can do focussed testing of specific parts of the **design**. ...

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 G Goossens, I Bolsens, B Lin, F Calthoor - IEEE/ACM International Conference on Computer-Aided Design, ..., 1994 - [ieeexplore.ieee.org](#)

 ... Figure 5: Outline of on **IC design** environment for ... more specific requirements for the different **design** tools ... level synthesis systems and software **compil-** ers for ...

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[Creating a mixed-signal simulation capability for concurrent ICdesign and test program development](#)

 T Austin, T Inc, CA Sunnyvale - Test Conference, 1993. Proceedings., International, 1993 - [ieeexplore.ieee.org](#)

 ... on a device, ;I critical element affecting **IC** performance could ... tool which test engineer uses to **compile** the test ... a means of combining the **design** simulator's ...

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 H Foster, S Uchitel, J Magee, J Kramer, IC London - IEEE International Conference on Web Services, 2004. ..., 2004 - [ieeexplore.ieee.org](#)

 ... 2BZ, UK {hf1, su2, jnm, jk}@doc.**ic**.ac.uk ... is easier in early phases of **design**, collaboration will ... into a model representation (FSP), 5) **compile** processes into ...

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IC Park, S Kang, Y Yi, D KAIST, S Korea - Computer Aided Design, 2003. ICCAD-2003. International ..., 2003 -

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... and Engineering Foundation through Lhc MICROS center and by **IC Design** Education Center ... instruction decoding from the simulation run-time to the **compile**-time [6 ...

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An intelligent analog **IC design** system based on manipulation of design equations

K Swings, G Gielen, W Sansen - Custom Integrated Circuits Conference, 1990., Proceedings of ..., 1990 -

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... 1. Introduction Designing full-custom **IC's** has generated ... are therefore the following :

t **Compile** analog functions ... forecast every unfeasible **design**, a redesign ...

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[PS] ► Logic Synthesis

S Design, V Netlist, RTL C Prepare, C Design - ics.ele.tue.nl

... RAMs, registers, Page 3. **IC design** and logic synthesis 3 basic logic cells, and regularly structured data **path** components. Its result ...

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M IbrahimAl-Behwashi, A El-Maleh, F Semester - citeseerx.ist.psu.edu

... Synthesis : is the process that generates a gate-level netlist for an **IC design** that has ... Performing a High-Effort **Compile**: - Pushes **Design** Compiler to ...

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Key authors: **J Southard** - **H Foster** - **B Murray** - **R Jain** - **S Uchitel**

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